

Rad-Hard and ULP FPGA with "Full" Functionality, Phase I

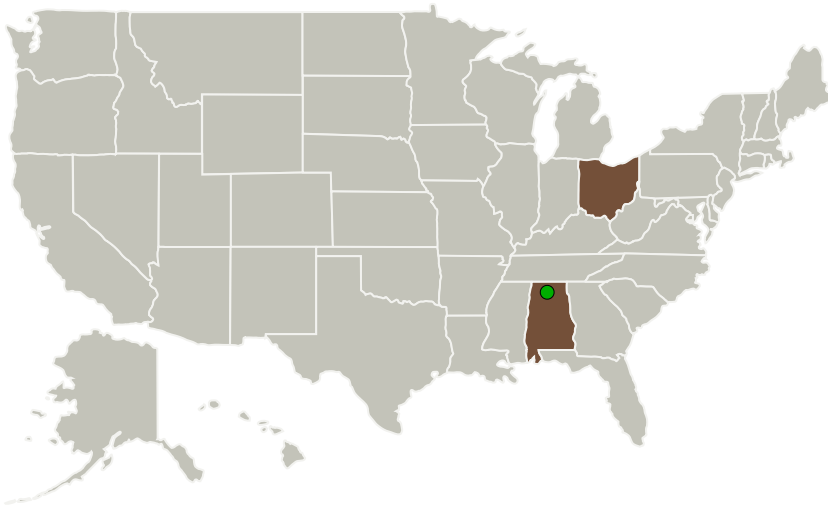
Completed Technology Project (2011 - 2011)



Project Introduction

Our vision for this NASA proposal is to develop a rad-hard and ultra low power (ULP) reconfigurable FPGA that will have

Primary U.S. Work Locations and Key Partners



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Organizations Performing Work	Role	Type	Location
RNET Technologies, Inc.	Lead Organization	Industry	Dayton, Ohio
● Marshall Space Flight Center(MSFC)	Supporting Organization	NASA Center	Huntsville, Alabama

Primary U.S. Work Locations

Alabama	Ohio
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Project Transitions



February 2011: Project Start

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September 2011: Closed out

Closeout Documentation:

- Final Summary Chart(<https://techport.nasa.gov/file/140234>)

Organizational Responsibility

Responsible Mission Directorate:

Space Technology Mission Directorate (STMD)

Lead Organization:

RNET Technologies, Inc.

Responsible Program:

Small Business Innovation Research/Small Business Tech Transfer

Project Management

Program Director:

Jason L Kessler

Program Manager:

Carlos Torrez

Principal Investigator:

Todd S Grimes

Co-Investigator:

Todd Grimes

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Technology Maturity (TRL)

Start: **2**
Current: **4**
Estimated End: **4**



Technology Areas

Primary:

- TX10 Autonomous Systems
 - └ TX10.3 Collaboration and Interaction
 - └ TX10.3.4 Operational Trust Building

Target Destinations

The Moon, Mars, Outside the Solar System, The Sun, Earth, Others Inside the Solar System